



HAL
open science

Optimization of the Noise Transfer Function of Extended-Frequency-Band-Decomposition sigma-delta A/D converters

Ali Beydoun, Philippe Benabes, Jacques Oksman

► **To cite this version:**

Ali Beydoun, Philippe Benabes, Jacques Oksman. Optimization of the Noise Transfer Function of Extended-Frequency-Band-Decomposition sigma-delta A/D converters. IEEE-NEWCAS TAISA Traitement Analogique de l'Information, du Signal et ses Applications., Jun 2008, Montreal, Canada. pp. 193-196, 10.1109/NEWCAS.2008.460354 . hal-00270096

HAL Id: hal-00270096

<https://hal-centralesupelec.archives-ouvertes.fr/hal-00270096>

Submitted on 3 Apr 2008

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

Optimization of the Noise Transfer Function of Extended-Frequency-Band-Decomposition sigma-delta A/D converters

Ali Beydoun, Philippe Benabes, Jacques Oksman

Abstract—Frequency-Band-Decomposition (FBD) is a good candidate to increase the bandwidths of ADC converters based on sigma-delta modulators. Each modulator processes a part of the input signal band and is followed by a digital filter. In the case of large mismatches in the analog modulators, a new solution, called Extended Frequency-Band-Decomposition (EFBD) can be used. This solution allows for, for example, a four percent error in the central frequencies without significant degradation in the performance when the digital processing part is appeared to the analog modulators. A calibration of the digital part is thus required to reach these theoretical performance. This paper will focus on a self-calibration algorithm for an EFBD. The algorithm helps minimize the quantization noise of the EFBD.

I. INTRODUCTION

The Frequency-Band-Decomposition (FBD) [1], [2], [3] is a natural way to widen the bandwidth of sigma-delta converters [4], using parallel bandpass modulators, where each modulator processes a part of the input signal band [5]. The main issue of such a solution is its high sensitivity to the central frequencies of the bandpass modulators. Extended Frequency-Band-Decomposition (EFBD) [5] makes it possible to convert a band-limited signal, using $N + 2$ parallel continuous-time bandpass modulators. This solution is able to adapt to the analog mismatches of the modulators caused by variations in the manufacturing processes, in order to minimize the quantization noise of the system and to allow for the reconstruction of the input signal by a transfer function which is as close as possible to a simple delay (minimum in-band ripple and linear phase).

As in many conversion systems, a calibration of the digital part of the EFBD becomes unavoidable. In the case of Time-Interleaved converters, gain and offset errors induce unwanted tones that can be minimized by an adequate calibration [6] [7], [8]. Such a calibration is performed by applying constant signals to each modulator and deducing the gain and offset from the filter outputs.

One of the advantages of the FBD solutions is that linear mismatches do not produce spurious tones. They only result in an in-band ripple and in an increase of the quantization noise. Using continuous-time modulators helps increase the sampling frequency, and induces natural filtering in the work band of each modulator. The calibration proposed in [8] for FBD can only deal with offset and gain, which is not sufficient for EFBD solutions. An algorithm that can calibrate the digital part of an EFBD without requiring heavy computing resources is proposed in this paper.

The second section of this paper gives the theoretical elements of the calibration method. The third section presents the calibration algorithm : the output mean power is minimized by modifying the bandwidths of the filters, the input being simply grounded. Finally, the fourth section concludes with the complexity and the duration of the whole algorithm.

II. OPTIMIZATION OF EFBD NOISE TRANSFER FUNCTION

An EFBD is composed of $N + 2$ parallel sigma-delta modulators (Fig. 1 - part A), where N is the number of modulators required to process the input signal band $[f_1 \dots f_2]$. Two extra modulators are used in the case of large analog mismatches so that the useful band $f_1 \dots f_2$ remains within the work band of the EFBD. The outputs of all channels are merged by a digital system (part B) to reconstruct the input signal.

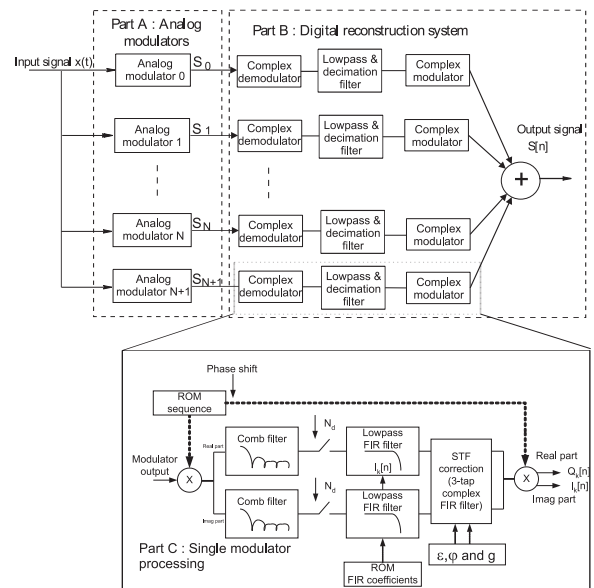


Fig. 1. Extended Frequency Band Decomposition architecture

The digital processing associated with each modulator is composed of a demodulation that brings the signal to base-band (demodulation frequencies are denoted f_C^k), of a comb filter that performs a decimation, of a FIR filter $H_k(z)$ that removes the out-of-band noise, of a signal transfer function correction $C_1(z)$ and of a modulation. The complete digital processing for one modulator is summarized in Fig. 1 - part C. The frequencies used for demodulation and modulation are

expressed as rational numbers so that the sequences are finite and can be stored in a ROM [5].

The NTF optimization of the system consists in using, for each frequency band, the modulator that has the best signal-to-noise ratio. As the signal transfer function is usually quite flat in the work band of the modulator, the modulator which is to be used for each frequency is the one whose noise power density is the lowest at this frequency.

Fig. 2 gives as an example, the noise power densities of each modulator (NTF^k), where all resonator frequencies differ from their ideal values by a constant value plus random mismatches (modelling process non-idealities). The Q factors of the filters are equal to 50. The boundaries between the band of each modulator are represented by vertical lines. In this example ($f_1 = 0.2, f_2 = 0.3$), the 10th modulator is not used as its band is completely outside the signal band.

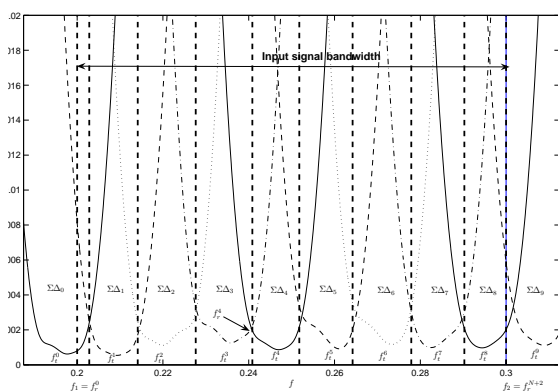


Fig. 2. Boundaries with non-ideal modulators

The global system quantization noise power is the sum of contributions of each modulator: $P_{NTF_t} = \sum_{k=0}^{N+1} P_{NTF^k}$.

$$P_{NTF^k} = \int_{f=-\frac{1}{2}}^{\frac{1}{2}} \left| H^k(e^{2j\pi N_d(f-f_c^k)}) \right|^2 \left| NTF^k(e^{2j\pi N_d f}) \right|^2 \Gamma_k(f) df \quad (1)$$

N_d is the decimation ratio, $\Gamma_k(f)$ is the quantization noise power spectral density and can be assumed constant [10]. $H_k(f)$ is the k^{th} FIR filter transfer function. It is assumed that the decimation comb filter removes sufficiently the folded noise produced by aliasing.

Fig. 3 gives as an example the magnitude of the product $\left| H^k(e^{2j\pi N_d(f-f_c^k)}) \right|^2 \left| NTF^k(e^{2j\pi N_d f}) \right|^2$ for $k = 4$. Most of the noise energy remaining after filtering lies between the modulator boundaries (vertical lines).

Thus it can be supposed that each FIR filter suppresses all the out-of-band noise, which leads to:

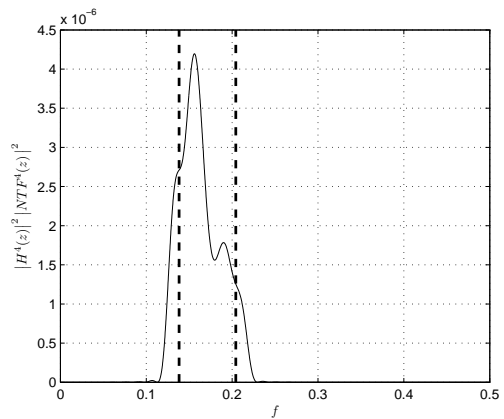


Fig. 3. Noise power spectrum density after filtering

$$P_{NTF^k} = \int_{f=f_r^k}^{f_r^{k+1}} \left| NTF^k(e^{2j\pi f}) \right|^2 \Gamma(f) df \quad (2)$$

f_r^k and f_r^{k+1} being the actual boundaries for the use of modulator k ,

$$\Gamma_k(f) = \Gamma = \frac{1}{3 \times 4^{Nbit}}, \quad (3)$$

where $Nbit$ is the number of bits of the ADC within each sigma delta modulator. All sub-bands are continuous, and $f_r^0 = f_1, f_r^{N+1} = f_2$.

f_t^k are constant frequencies chosen as the middle of the band of each modulator k ; the global noise power can be expressed:

$$P_{NTF_t} = \Gamma \sum_{k=0}^{N+1} \int_{f=f_r^k}^{f_r^{k+1}} \left| NTF^k(e^{2j\pi f}) \right|^2 df \quad (4)$$

Each integral is split into two terms that have at least one constant boundary. The first and the last term in the sum are separated as they are constant.

$$P_{NTF_t} = \Gamma \int_{f=f_1}^{f_t^0} \left| NTF^0(e^{2j\pi f}) \right|^2 df + \Gamma \sum_{k=1}^{N+1} \left(\int_{f=f_t^{k-1}}^{f_r^k} \left| NTF^{k-1}(e^{2j\pi f}) \right|^2 df + \int_{f=f_r^k}^{f_t^k} \left| NTF^k(e^{2j\pi f}) \right|^2 df \right) + \Gamma \int_{f=f_t^{N+1}}^{f_2} \left| NTF^{N+1}(e^{2j\pi f}) \right|^2 df \quad (5)$$

The total noise power is then the sum of two constant values, plus $N + 1$ terms, each of them depending on one

of the boundaries f_r^1 to f_r^{N+1} . Each boundary can then be optimized independently from the others. Fig. 4 shows as an example $|NTF^3|$, $|NTF^4|$ and the term in Eq. (5) where f_r^4 is involved. The criterion appears reasonably convex and smooth.

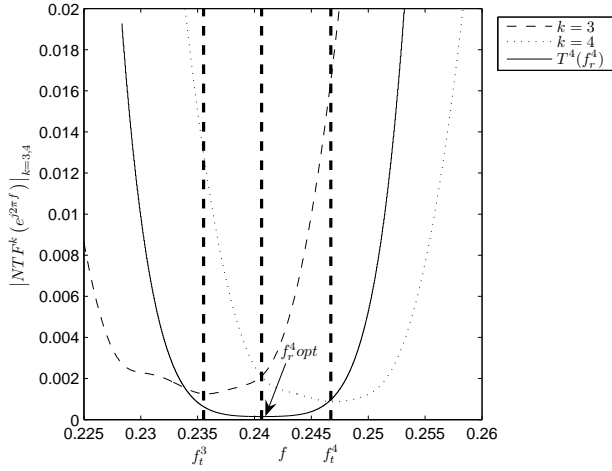


Fig. 4. Boundary optimization criterion

It was verified that this property remains true when taking into account the FIR filter effect, resulting in a behavior similar to that of Fig. 4.

III. NOISE TRANSFER FUNCTION CALIBRATION ALGORITHM

The noise transfer function is optimized by tuning the boundaries between modulators f_r^k , $k = 1..N+1$ from f_r^{k-1} to f_r^{k+1} . The frequencies used for the modulator and demodulator are deduced from these values:

$$f_C^k = (f_r^{k-1} + f_r^k)/2, \quad (6)$$

and the bandwidths of the lowpass filters.

$$\Delta f_k = \frac{f_r^k - f_r^{k-1}}{2} \text{ for } k = 1..N. \quad (7)$$

It was shown in [5] that an error of 4% in the width of the subband (0.05% of the sampling frequency) causes a resolution loss less than 0.1 bit. Thus, the boundary frequency values (f_r^k) can be quantized in this example with a step $q_s = F_s/1024$. The whole band represents 102 steps and the bandwidth of each subband would be 12 or 13 steps if all modulators were ideal.

The input of the EFBD must be first grounded. The noise power produced by the quantization is minimized by varying the values of the boundary frequencies f_r^k . The demodulation sequence and the lowpass FIR filters are fully determined by the knowledge of these values ((6) and (7)).

The adaptation scheme given in Fig. 5 was used. The scheme itself uses the processing of the digital part presented in Fig. 1. The estimate of the converter noise power is:

$$\hat{P} = \frac{1}{N_s} \sum_{n=1}^{N_s} \sum_{k=1}^{N+2} (I_k[n]^2 + Q_k[n]^2) \quad (8)$$

N_s is the number of samples used for the power estimation, I and Q are the outputs of each filter (Fig. 1).

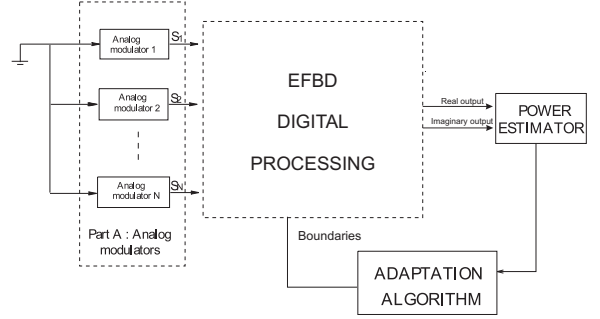


Fig. 5. Quantization noise minimization

The proposed algorithm is based on a so-called relaxation method. It does not require any operations excepted the calculation of the output power. Other faster algorithms could be used [9], but lead to more complex logic. Initial values are those obtained theoretically (i.e. design typical values). The algorithm changes iteratively the boundary value f_r^k between f_r^{k-1} and f_r^{k+1} for $k = 1$ to $N + 1$ in each sequence. The first boundary remains f_1 and the last boundary remains f_2 . This algorithm can be expressed for one sequence as:

```

affect to  $f_r^k, f_C^k, \Delta f_k$  their theoretical values
for  $k$  from 1 to  $N + 1$ 
  for  $f_r^k$  from  $f_r^{k-1}$  to  $f_r^{k+1}$ 
    calculate  $f_C^k$ 
    calculate  $\Delta f_k$ 
    apply these values to the filter
    calculate the noise power  $N_p(f_r^k)$ 
  end for
  get  $f_r^k opt = \text{argmin}_k(N_p(f_r^k))$ 
  calculate  $f_C^k$  and  $\Delta f_k$  from  $f_r^k opt$ 
end for

```

The output power is estimated for each boundary value and the value that minimizes the output power is memorized.

The quantization noise power is estimated from a finite number of samples, leading to an error in the estimation. Due to this error, the algorithm will not converge to the optimum value in one step. It has to be applied several times to be sure to reach the optimum point. When 2^{12} samples are used for the quantization noise power calculation, two or three sequences are required to reach the optimum.

This algorithm has been performed on a simulation of the bank described in Fig. 2 with 10 modulators. The output signals of the modulators have been memorized so that the same outputs are used for each loop. The results are given on Fig. 6, which shows the resolution of the EFBD (calculated from the estimated noise power) after each iteration. It may

be seen that the optimum value is reached at the end of the first sequence.

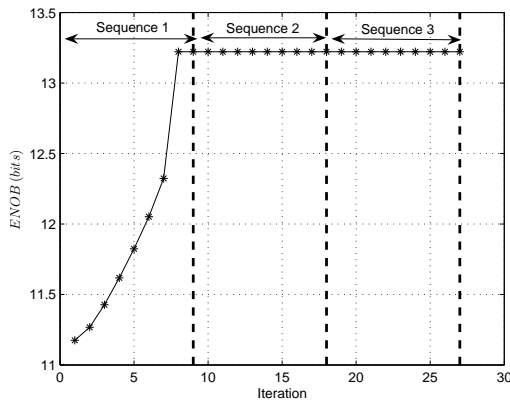


Fig. 6. Evolution of noise power during iterations -off line-

This algorithm has also been tested in a simulation where the outputs of the modulators were calculated for each iteration. The result is given in Fig. 7. It can be seen that the optimum is reached after three sequences. We verified by simulations that the convergence to the optimum value is also reached after three sequences for all kind of mismatches (with systematic or random errors on the modulator's central frequencies). The method is, therefore relevant.

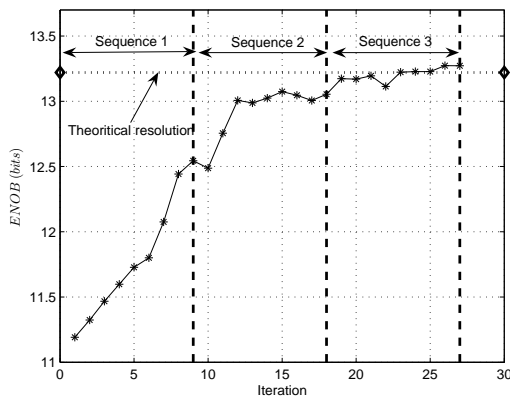


Fig. 7. Evolution of noise power during iterations -on line-

With an 800 MHz analog frequency, each noise power calculation performed from 4000 samples takes $5 \mu\text{s}$ to perform (the OSR of the system is equal to 5), and each sequence in the worst case approximately 1.12 ms (9 boundaries are varied among 25 values). The total NTF calibration time for 3 sequences can be estimated to 3.3 ms.

IV. CONCLUSION

This paper describes an auto-calibration algorithm for an EFBD sigma-delta converter. The main advantage of this algorithm is to use the implemented digital filter plus a reasonable amount of logic. A power estimator, associated

with counters and a sequencer is needed. The logic quantity associated with this estimator can be neglected compared with the main filter. The whole calibration process proposed in this paper can be performed in few ms, which is compatible with the power-up time of any communication system. Anyway, the possibility of performing a power-up adjustment have shown the feasibility of a EFBD converter, without requiring a strict analog trimming.

REFERENCES

- [1] P. Aziz, H. Sorensen, and J. Van der Spiegel, "Multiband sigma-delta modulation," *Electronics Letters*, pp. 760–762, April 1993.
- [2] P. Aziz, H. Sorensen, and J. Van der Spiegel, "Multiband sigma-delta analog to digital conversion," *ICASSP*, vol. 3, pp. 249–252, April 1994.
- [3] A. Eshraghi and T. Fiez, "A comparative analysis of parallel delta-sigma ADC architectures," *IEEE Trans. Circuit and Sys.I*, vol. 51, pp. 450–458, March 2004.
- [4] R. Schreier and G.C. Temes, *Understanding Delta-Sigma Data Converters*. New Jersey: Wiley, 2005, chap. 4.
- [5] P. Benabes, A. Beydoun, and R. Kielbasa, "Dimensionnement prliminaire d'un banc de convertisseurs sigma-delta parallles," *Taisa 2005*, pp. xxx, 2005 2007.
- [6] V. Ferragina, A. Fornasari, U. Gatti, P. Malcovati, F. Maloberti, "Gain and offset mismatch calibration in time-interleaved multipath A/D sigma-delta modulators," *IEEE Transactions on Circuits and Systems I: Fundamental Theory and Applications*, vol. 51, pp. 2365–2373, Dec. 2004.
- [7] A. Delic-Ibukic, D.M. Hummels, "Continuous gain calibration of parallel delta sigma A/D converters," *IEEE Instrumentation and Measurement Technology Conference*, pp. 905–909, April 2006.
- [8] R.D. Batten, A.Eshraghi, T.S. Fiez, "Calibration of parallel $\Sigma\Delta$ ADCs," *IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing*, vol. 49, pp. 390–399, June 2002.
- [9] W.H. Press, B.P. Flannery, S.A.Teukolsky, W.T.Vetterling, *NUMERICAL RECIPES, The art of scientific computing*. Cambridge University Press 1986.
- [10] W.R. Bennett, "Spectra of quantized signals," *Bell syst. tech. J.*, vol. 27, pp. 446–472, 1948.
- [11] G. Appel, "Fractional N synthesizers," *R.F. Design*, vol. 23, pp. 34–50, Nov. 2000.
- [12] P.V. Brennan, R. Walkington, "Stored-sequence sigma-delta fractional-N synthesizer," *IEE Circuits, Devices and Systems*, vol. 151, n 2, pp. 69–73, April 2004.
- [13] M. Ortmanns and F. Gerfers, *Continuous-Time Sigma-Delta A/D Conversion*. Berlin: Springer, 2006.
- [14] C. Lelandais-Perault, P. Benabes, J.L. De Gouy, and R. Kielbasa, "A parallel structure of a continuous-time filter for band-pass sigma-delta A/D converters," *10th IEEE International Conference on Electronics, Circuits and Systems*, pp. 14–17, December 2003.
- [15] S. Benabid, E. Najafi-Aghdam, P. Benabes, S. Guessab, R. Kielbasa, "CMOS design of a multibit bandpass continuous-time sigma delta modulator running at 1.2 GHz," *IEEE International Caracas Conference on Devices, Circuits and Systems*, pp. 51–55, November 2004.