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Modeling and Control of HVDC Transmission Systems From Theory to Practice and Back

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Abstract

The problem of modeling and control of multi-terminal high-voltage direct-current transmission systems is addressed in this paper, which contains five main contributions. First, to propose a unified, physically motivated, modeling framework—based on port-Hamiltonian representations—of the various network topologies used in this application. Second, to prove that the system can be globally asymptotically stabilized with a decentralized PI control, that exploits its passivity properties. Close connections between the proposed PI and the popular Akagi’s PQ instantaneous power method are also established. Third, to reveal the transient performance limitations of the proposed controller that, interestingly, is shown to be intrinsic to PI passivity-based control. Fourth, motivated by the latter, an outer-loop that overcomes the aforementioned limitations is proposed. The performance limitation of the PI, and its drastic improvement using outer-loop controls, are verified via simulations on a three-terminals benchmark example. A final contribution is a novel formulation of the power flow equations for the centralized references calculation.

Keywords: multi-terminal HVDC transmission systems; passivity-based control; port-Hamiltonian systems; PI control; nonminimum-phase systems; PQ and DC voltage control; performance limitations; power flow equations.

1. Introduction

In the last few years it has been observed an ever widespread utilization of renewable energy utilities, mainly based on wind and solar power [18, 7]. Because of its intermittent nature the integration of this generating units to the existing alternating-current (AC) distribution network poses a challenging problem [6, 24]. For this, and other reasons related to reduced losses and problems with reactive power and voltage stability in AC systems, the option of high-voltage direct-current (HVDC) transmission systems is gaining wide popularity, see [7, 22, 20] for additional motivations and details.

The main components of an HVDC system are AC to DC power converters, transmission lines and voltage bus capacitors. The power converters connect the AC sources—that are associated to renewable generating units or to AC grids—to an HVDC grid through voltage bus capacitors. Two notable features distinguish HVDC systems from standard AC ones: the absence of a global signal (the synchronization frequency) and the central role played by the power converters, the dynamics of which are highly *nonlinear*.

For its correct operation, HVDC systems—like all electrical power systems—must satisfy a large set of different regulation objectives that are, typically, associated to the multiple time-scale behavior of the system. One way to deal with

this issue, that prevails in practice, is the use of hierarchical architectures. These are nested control loops, at different time scales, each one providing references for an inner controller [21, 38]. This paper focuses mainly on the “innermost” control loop for HVDC transmission systems, that is, the control at the power converter level—in the sequel referred as *inner-loop* control. The objective of the inner-loop control is to asymptotically drive the HVDC system towards a desired steady-state regime determined by the user. Regulation should be achieved selecting a suitable switching policy for the converters. A major practical constraint is that the control should be *decentralized*. That is, the controller of each power converter has only available for measurement its corresponding coordinates, with no exchange of information between them.

Starting from single AC/DC converter models many strategies have been proposed for the inner-loop control of the power converters used in HVDC systems [2, 23, 26], with the dominating structure consisting of nested PI loops: an inner current control loop and an outer loop to regulate the capacitor voltage. The rationale used to justify this structure is the time-scale separation between currents and voltages. However, with the notable exception of [34], the performance claims are not corroborated by rigorous stability proofs. Because of the absence of theoretical analysis, a time-consuming and expensive procedure to tune the gains of the PIs is then required to complete the design. This is typically done based on the linearization of the system that, because of the highly nonlinear behavior of the converters and the wide range of the operating regimes, often yields below-par performances.

The main objective of this paper is to contribute, if modestly,

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towards the development of a general, theoretically-founded procedure for the modeling, analysis and control of HVDC systems. With the intention to bridge the gap between theory and applications, one of the main concerns is to establish connections between existing engineering solutions, usually derived via *ad-hoc* considerations, and the solutions stemming from theoretical analysis. In particular, it is shown that modifying the theoretically-based inner-loop controller to incorporate the standard considerations of outer-loop control considerably improves its transient performance.

The contributions of the paper are the following.

- (C1) To propose a unified, physically motivated, modeling framework of the various network topologies used in HVDC systems. This framework is based on port-Hamiltonian (pH) models of the system components [11, 35, 40] combined with a suitable graph theoretic representation of their interconnection [12]. The lines are modeled as simple series resistance-inductance (*RL*) circuits and the capacitors are assumed to be leaky elements, all components being linear. Although many different kinds of power converters are used in applications the dominant structure is the so-called voltage-source rectifier (VSR), which are the ones considered in the paper. The network is described via a *meshed topology*, which allows for possible direct connection of the VSRs with the transmission lines.
- (C2) In the spirit of [16, 19, 29] it is proved that the incremental model of the VSR defines a *passive* map with respect to some suitably designed output. A consequence of this fundamental property is that a decentralized PI passivity-based controller (PBC) *globally asymptotically stabilizes* (GAS) any assignable equilibrium, with no restriction imposed on the (positive) gains of the PI-PBC. It is also shown that the proposed PI-PBC is closely related with Akagi's PQ instantaneous power method [2] that was derived (without a stability analysis) invoking power balance considerations and is standard in applications.
- (C3) It is well-known that passive systems are minimum phase and have relative degree one [5, 35]. Consequently, the attainable performance of a PI-PBC is limited by its associated zero dynamics. Another contribution of the paper is the proof that, in HVDC systems, the zero dynamics is "extremely slow", stymying the achievement of fast transient responses. On the other hand, it is also shown that other inner-loop PI controllers reported in the literature may exhibit unstable behavior because the zero dynamics associated to the corresponding outputs are *non-minimum phase*.
- (C4) Common engineering practice is followed to improve the transient performance, by adding an outer-loop that determines the PI-PBC reference signals—the widely diffused *droop control* [3, 31]. After revisiting its standard formulation, a modification of the standard PI-PBC is proposed, showing that the intrinsic performance limitations are overcome, further preserving global asymptotic stability. The

drastic improvement with this outer controller is finally verified via simulations on a three-terminals benchmark example.

- (C5) A final contribution relates to the design of the last outer-loop controller in terms of a centralized reference calculator. Although there is no universal agreement to define the tasks of this control loop it usually relates to the regulation of the flow of active and reactive power to be injected into the network while keeping the voltage of the capacitors near a desired constant value. Most popular approaches, which usually invoke *ad-hoc* considerations, are reviewed and contextualized in the present framework.

The remaining part of the paper is structured as follows. In Section 2, the mathematical model of the system is established (C1). Then, to determine the achievable behaviors, a study of the assignable equilibria is necessary. This analysis is done in Section 3. The main contribution (C2) is next developed in Section 4, with the design of the decentralized passivity-based PI controller. Slow transients exhibited in simulations motivate the subsequent performance analysis (C3), that is carried out in Section 5. Sections 6 and 7 are then devoted to revisit standard outer-loop controllers (C4) and the problem of references calculation (C5). Conclusions and future work follow then in Section 8.

Notation All vectors are column vectors. Given positive integers n, m , symbols $\underline{0}_n \in \mathbb{R}^n$ denotes the vector of all zeros, $\mathbb{1}_n \in \mathbb{R}^n$ the vector with all ones, \mathbb{I}_n the $n \times n$ identity matrix, $\underline{0}_{n \times m}$ the $n \times m$ column matrix of all zeros. $x := \text{col}(x_1, \dots, x_n) \in \mathbb{R}^n$ denotes a vector with entries $x_i \in \mathbb{R}$, when clear from the context it is simply referred as $x := \text{col}(x_i)$. $\text{diag}\{a_i\}$ is a diagonal matrix with entries $a_i \in \mathbb{R}$ and $\text{bdiag}\{A_i\}$ denotes a block diagonal matrix with entries the matrices A_i . For a function $f: \mathbb{R}^n \rightarrow \mathbb{R}$, ∇f denotes the transpose of its gradient. The subindex i , preceded by a comma when necessary, denotes elements corresponding to the i -th subsystem.

2. Energy-based Modeling

In [12] it was shown that electrical power systems can be represented by a directed graph¹ where the relevant electrical components correspond to edges and the buses correspond to nodes. Moreover, to underscore the physical structure of the components, they are modeled as pH systems. In this section the same procedure is applied to describe the dynamics of HVDC transmission systems.

2.1. Assumptions

As indicated in the Introduction, the relevant components for an HVDC transmission system are: VSRs, *RL* transmission

¹A directed graph is an ordered 3-tuple, $\mathcal{G} = \{\mathcal{V}, \mathcal{E}, \Pi\}$, consisting of a finite set of nodes \mathcal{V} , a finite set of directed edges \mathcal{E} and a mapping Π from \mathcal{E} to the set of ordered pairs of \mathcal{V} , where no self-loops are allowed.

lines and voltage bus capacitors. Throughout the paper the following assumptions—which are widely accepted in practice—are made.

- (A1) Balanced operation of the three phase line voltages.
- (A2) Synchronized operation of the VSRs².
- (A3) Ideal four quadrant operation of the VSRs.

Assumptions A1 and A2 considerably simplify the modeling and control problems, as they allow the description of the three-phase dynamics of the VSRs in suitably oriented $dq0$ reference frames, where the value of the 0-component is always zero, thus reducing the three AC quantities to two DC quantities. Consequently, it is possible to express the regulation objective as a standard *equilibrium stabilization* problem of the nonlinear dynamical system describing the behavior of the HVDC system. Assumption A3 directly follows by assuming an HVDC transmission system based on VSRs instead of current source rectifiers, which is an alternative converter topology used in HVDC systems. As a matter of fact, since the VSRs do not depend on line-commutations, all the four quadrants of the operating plane are possible, hence Assumption A3 is automatically satisfied for the system under consideration [1].

2.2. Network topologies: A graph description

It is possible to distinguish two kinds of topologies used in HVDC transmission systems: *radial* and *meshed* topology [13, 4, 15], which are illustrated in Fig. 1. The radial topology is widely used for systems in which a certain number of off-shore stations feeds on-shore stations with no connection between them. This is the case for example of on-shore stations situated on opposite seacoasts while the off-shore stations are placed in their middle [4, 22]. However, in a more general setting one has to consider the situation in which the stations are directly connected with lines, that corresponds to a meshed topology. In the interest of brevity, a systematic way to build global pH models is presented only for the meshed topology. For a radial topology, analogous results can be obtained, for which the interested reader is referred to [41].

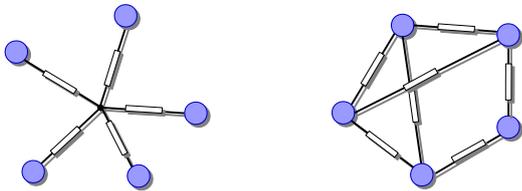


Figure 1: Nodal representation of HVDC transmission systems with radial and meshed topologies.

In order to give a formal representation of a topology the following definitions are adopted. A bus is called a *VSR-bus* if a

²Synchronized operation of the VSRs is usually achieved via robust phase-locked-loop detection of the latching frequencies [38].

VSR is connected to it and a bus is called a *capacitor-bus* when only a capacitor is connected to it. Furthermore, a bus is called a *reference-bus* when all the voltages of the buses in the network are measured with respect to it. As the reference-bus is assumed to be at ground potential, it is also denoted as *ground*. A general topology is then described by the incidence matrix \mathcal{M} associated to the graph, where the nodes represent the ground, the VSR and the capacitor-buses; the edges represent the VSRs, the lines and the single capacitors that are interconnected to the ground or to the voltage buses.

In a meshed topology, each VSR is connected to the ground and to a VSR-bus, while the lines directly connect VSR-buses, according to a determined meshed structure. The number n of VSRs is the same as voltage buses, ground excluded, and is lower or equal to the number ℓ of lines. Formally, this can be represented by a graph $\mathcal{G} := \{\mathcal{V}, \mathcal{E}, \Pi\}$ constituted by: $n + 1$ ordered nodes, where n nodes are associated to the VSR-buses and one node to the ground; $n + \ell$ ordered edges, where n edges are associated to the VSRs and ℓ edges to the lines. The incidence matrix then — following the mentioned order — takes the form

$$\mathcal{M} = \begin{bmatrix} \mathbb{I}_n & M \\ -\mathbb{1}_n^\top & \mathcal{Q}_\ell^\top \end{bmatrix} \in \mathbb{R}^{(n+1) \times (n+\ell)}, \quad (2.1)$$

where M is the incidence matrix of the subgraph obtained by eliminating the VSR edges and the ground node.

Remark 2.1. In a meshed topology the only relevant components are the VSRs and the RL transmission lines. As a matter of fact, because a VSR is associated to each node, the voltage bus capacitors can be represented by an equivalent VSR output capacitor, that results to be the parallel interconnection of all capacitors attached to the node.

2.3. Port-Hamiltonian models of the elements

As explained above, the edges of the graph \mathcal{G} contain the electrical components of the HVDC system, namely n VSRs and ℓ RL transmission lines, while the nodes are the buses. In this section, a pH representation of each of these elements is derived, which are then interconnected—through power preserving interconnections—via the graph. Besides its physically appealing nature, the choice of a pH model is motivated by the fact that—similarly to [16]—this structure is instrumental to derive the passivity property exploited in the controller design. To enhance readability the models of the VSRs and the transmission lines are presented separately.

2.3.1. Voltage source rectifiers

In [11, 16, 41] the well-known average model of a single VSR shown in Fig. 2, expressed in dq -coordinates and written in (perturbed) pH form is given. Similarly, a *set* of n VSRs can also be represented in pH form as

$$\begin{aligned} \dot{x}_R &= [\mathcal{J}_R(u) - \mathcal{R}_R] \nabla \mathcal{H}_R + E_1 V - E_3 i_R \\ v_R &= E_3^\top \nabla \mathcal{H}_R, \end{aligned} \quad (2.2)$$

with the following definitions.

- State space variables the collection of inductors fluxes $(\phi_{d,i}, \phi_{q,i})$ and capacitor charges $q_{c,i}$ of every VSR, that is, $x_R := \text{col}(\text{col}(\phi_{d,i}), \text{col}(\phi_{q,i}), \text{col}(q_{c,i})) \in \mathbb{R}^{3n}$.

- Energy function

$$\mathcal{H}_R(x_R) := \frac{1}{2} x_R^\top Q_R x_R, \quad Q_R := \text{bdiag}\{L_R^{-1}, L_R^{-1}, C_R^{-1}\},$$

with

$$L_R := \text{diag}\{L_{r,i}\}, \quad C_R := \text{diag}\{C_{r,i}\},$$

where $L_{r,i}, C_{r,i}$ are the inductance and capacitance of each VSR, respectively³.

- Duty cycles $u := \text{col}(u_{Rd}, u_{Rq}) \in \mathbb{R}^{2n}$, where $u_{Rd} := \text{col}(u_{d,i})$ and $u_{Rq} := \text{col}(u_{q,i})$.

- External voltage sources $V := \text{col}(v_{d,i}) \in \mathbb{R}^n$, where $v_{d,i}$ is the d component of the AC sources. These voltages are assumed constant and positive.

- Port variables the out-going currents $i_R := \text{col}(i_{dc,i}) \in \mathbb{R}^n$ and the voltages $v_R := \text{col}(v_{dc,i}) \in \mathbb{R}^n$.

- Interconnection matrix

$$\mathcal{I}_R(u) := \sum_{i=1}^n (\mathcal{I}_{R0,i} L_{r,i} \omega_i + \mathcal{I}_{Rd,i} u_{d,i} + \mathcal{I}_{Rq,i} u_{q,i}) \quad (2.3)$$

where ω_i are the AC sides frequencies and

$$\mathcal{I}_{R0,i} := \begin{cases} -1 & \text{in } (i, n+i) \\ 1 & \text{in } (n+i, i) \\ 0 & \text{elsewhere} \end{cases}$$

$$\mathcal{I}_{Rd,i} := \begin{cases} 1 & \text{in } (i, 2n+i) \\ -1 & \text{in } (2n+i, i) \\ 0 & \text{elsewhere} \end{cases} \quad \mathcal{I}_{Rq,i} := \begin{cases} -1 & \text{in } (n+i, 2n+i) \\ 1 & \text{in } (2n+i, n+i) \\ 0 & \text{elsewhere} \end{cases}$$

- Dissipation matrix $\mathcal{R}_R := \text{bdiag}\{R_R, R_R, G_R\}$, where $R_R := \text{diag}\{R_{r,i}\}$ and $G_R := \text{diag}\{G_{r,i}\}$, with $R_{r,i}, G_{r,i}$ the resistance and conductance of each VSR.

- Port matrices $E_1 : [\mathbb{I}_n \ 0 \ 0]^\top$, $E_3 := [0 \ 0 \ \mathbb{I}_n]^\top \in \mathbb{R}^{3n \times n}$.

Remark 2.2. Note that, in view of the skew-symmetry of $\mathcal{I}_R(u)$, the VSRs satisfy the power balance equation

$$\underbrace{\mathcal{H}_R}_{\text{stored power}} = - \underbrace{x_R^\top Q_R \mathcal{R}_R Q_R x_R}_{\text{dissipated power}} + \underbrace{x_R^\top Q_R E_1 V - x_R^\top Q_R E_3 i_R}_{\text{supplied power}} \quad (2.4)$$

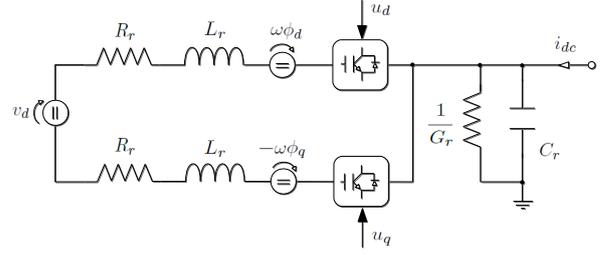


Figure 2: Schematic diagram of the equivalent circuit of a VSR in dq frame.

2.3.2. Transmission lines

A set of ℓ RL transmission lines can be represented by the pH system

$$\begin{aligned} \dot{x}_L &= -\mathcal{R}_L \nabla \mathcal{H}_L + v_L \\ i_L &= -\nabla \mathcal{H}_L, \end{aligned} \quad (2.5)$$

with the following definitions.

- State space variables the collection of inductor fluxes $x_L := \text{col}(\phi_{\ell,i}) \in \mathbb{R}^\ell$ of every line.

- Energy function

$$\mathcal{H}_L(x_L) := \frac{1}{2} x_L^\top Q_L x_L, \quad Q_L := \text{diag}\left\{\frac{1}{L_{\ell,i}}\right\},$$

where $L_{\ell,i}$ is the inductance of the line.

- Port variables the voltages at the terminals $v_L := \text{col}(v_{L,i}) \in \mathbb{R}^\ell$ and the inductors currents $i_L := \text{col}(i_{\ell,i}) \in \mathbb{R}^\ell$.

- Dissipation $\mathcal{R}_L = \text{diag}\{R_{\ell,i}\}$, with $R_{\ell,i}$ the resistance of the line.

2.4. Overall interconnected system

The interconnection laws can be obtained following the approach used in [36], where Kirchhoff's current and voltage laws (KCL and KVL, respectively) are expressed in relation to the incidence matrix. For a *meshed* topology then it follows

$$\begin{aligned} \text{[KCL]} \quad \mathcal{M} \mathcal{I}_e &= \mathbf{0}_{n+1}, \\ \text{[KVL]} \quad \mathcal{M}^\top \mathcal{V} &= \mathcal{V}_e, \end{aligned} \quad (2.6)$$

where $\mathcal{I}_e := \text{col}(i_R, i_L)$, $\mathcal{V}_e := \text{col}(v_R, v_L)$ and $\mathcal{V} := \text{col}(v_1, \dots, v_n)$, v_0 are the edge currents, the edge voltages, the nodes potentials and the ground potential, respectively. The ground potential $v_0 = 0$ by definition. From (2.6) and (2.1) then follows

$$\begin{aligned} i_R + M i_L &= \mathbf{0}_n, \quad -\mathbb{1}_n^\top i_R = 0, \\ v &= v_R, \quad M^\top v = v_L. \end{aligned} \quad (2.7)$$

Recalling the expression for i_L from (2.2) and v_R from (2.5) it is easy to get

$$i_R = M \nabla \mathcal{H}_L, \quad v_L = M^\top E_3^\top \nabla \mathcal{H}_R. \quad (2.8)$$

³Unless indicated otherwise all physical parameters of the system are positive constants.

To obtain the overall pH representation it is then sufficient to combine (2.2), (2.5) and (2.8), thus leading to

$$\dot{x} = [\mathcal{J}(u) - \mathcal{R}] \nabla \mathcal{H} + EV, \quad (2.9)$$

with the following definitions.

- State space variables $x := \text{col}(x_R, x_L) \in \mathbb{R}^{3n+\ell}$.
- Energy function $\mathcal{H}(x) := \mathcal{H}_R(x) + \mathcal{H}_L(x)$.
- Duty cycles (controls) $u := \text{col}(u_{Rd}, u_{Rq}) \in \mathbb{R}^{2n}$.
- Interconnection matrix

$$\mathcal{J}(u) := \begin{bmatrix} J_R(u) & -E_3 M \\ M^\top E_3^\top & \underline{0}_{\ell \times \ell} \end{bmatrix}, \quad (2.10)$$

- Dissipation matrix

$$\mathcal{R} := \text{bdiag}\{\mathcal{R}_R, \mathcal{R}_L\} > 0. \quad (2.11)$$

- Input matrix $E := [E_1^\top \underline{0}_{\ell \times n}^\top]^\top$.

Remark 2.3. To simplify the notation in the pH representation it is selected a state representation of the system using energy variables, that is, inductor fluxes and capacitor charges, instead of the more commonly used co-energy variables, *i.e.*, inductor currents and capacitor voltages. See (4.8) and [25] for the derivation of the pH model in the latter coordinates. The coordinates are indeed related by

$$i_d = \frac{\phi_d}{L}, \quad i_q = \frac{\phi_q}{L}, \quad v_C = \frac{q_C}{C}, \quad i_L = \frac{\phi_L}{L_\ell}. \quad (2.12)$$

Remark 2.4. For ease of presentation it is assumed that the state of the system lives in $\mathbb{R}^{3n+\ell}$. Due to physical and technological constraints it is actually only defined in a subset of $\mathbb{R}^{3n+\ell}$. In particular, the voltage of the DC link v_C is strictly bounded away from zero.

3. Assignable Equilibria

A first step towards the development of a control strategy for the system (2.9) is the definition of its achievable, steady-state behavior, which is determined by the assignable equilibria. That is, the (constant) vectors $x^* \in \mathbb{R}^{3n+\ell}$ such that

$$[\mathcal{J}(u^*) - \mathcal{R}] \nabla \mathcal{H}(x^*) + EV = \underline{0}_{3n+\ell}$$

for some (constant) vector $u^* \in \mathbb{R}^{2n}$. To identify this set the following lemmata are established.

Lemma 3.1. The equilibria of the transmission line coordinates are given by

$$x_L^* = (\mathcal{R}_L Q_L)^{-1} M^\top E_3^\top Q_R x_R^*. \quad (3.1)$$

Proof. Setting to zero the left-hand side of (2.5), calculated at x_L^* , gives

$$\underline{0}_\ell = -\mathcal{R}_L Q_L x_L^* + v_L^* \Rightarrow x_L^* = (\mathcal{R}_L Q_L)^{-1} v_L^*.$$

Moreover, from (2.8) it follows that $v_L^* = M^\top E_3^\top Q_R x_R^*$, that replaced in the equation above completes the proof. $\square \square \square$

Lemma 3.2. The equilibria of the VSRs coordinates are the solution of the n quadratic equations, $i = 1 \dots n$

$$-\frac{R_i}{L_{r,i}^2} \left[(\phi_{d,i}^*)^2 + (\phi_{q,i}^*)^2 \right] - \frac{G_i}{C_{r,i}^2} (q_{C,i}^*)^2 + \frac{v_{d,i}}{L_{r,i}} \phi_{d,i}^* - \frac{1}{C_{r,i}} q_{C,i}^* i_{dc,i}^* = 0, \quad (3.2)$$

with $\text{col}(i_{dc,i}^*) = M \mathcal{R}_L^{-1} M^\top \text{col}(q_{C,i}^*)$.

Proof. In [28] it is shown that the set of admissible equilibria of a VSR is obtained by setting equal to zero the power balance of the VSR, that for n VSRs is equivalent to (3.2). To complete the proof, it is now sufficient to recall definitions

$$\text{col}(i_{dc,i}^*) = i_R^*, \quad E_3^\top Q_R x_R^* = \text{col}(q_{C,i}^*),$$

together with (2.8), (3.1). $\square \square \square$

The main result of the section is now presented, the proof of which follows immediately from the lemmata above.

Proposition 3.3. The set of assignable equilibria of the system (2.9) is given by

$$\mathcal{E} := \{x^* \in \mathbb{R}^{3n+\ell} \mid (3.1) \text{ and } (3.2) \text{ hold}\}. \quad (3.3)$$

From the derivations above it is clear that the equilibria of the network are univocally determined by the equilibria of the VSRs. Moreover, the latter should satisfy the quadratic equations (3.2), which are the well-known *power flow steady-state equations* (PFSSSE) of the individual VSR subsystems. A question of interest is how to select from this set the equilibrium points that correspond to some *desired behavior*. In the latter definition there are many practical considerations to be taken into account, the discussion of which is postponed to Sections 6 and 7.

Remark 3.4. It is well-known that for affine systems of the form $\dot{x} = f(x) + g(x)u$ the assignable equilibrium set is given by

$$\{x^* \in \mathbb{R}^n \mid g^\perp(x^*) f(x^*) = 0\}$$

where $g^\perp(x)$ is a full-rank left annihilator of $g(x)$. Moreover, given x^* , the corresponding equilibrium control u^* is *univocally* determined by

$$u^* = - \left[(g^\top g)^{-1} g^\top f \right] (x^*).$$

Since (2.9) is clearly of this form this relations hold true for the HVDC transmission system under study. See [28] for additional details on this issue.

Remark 3.5. Differently from the single VSR case, the set of assignable equilibria does not coincide, but is strictly contained,

in the set where the power of the system is balanced, that is

$$\mathcal{E} \subset \mathcal{P}, \quad \mathcal{P} := \{x^* \in \mathbb{R}^{3n+\ell} \mid \mathcal{H}_R = 0\}.$$

This fact is clearly explained in [28], where it is proved that a necessary condition for $\mathcal{E} \equiv \mathcal{P}$, is the system to be of co-dimension one.

4. Main Result: Inner Loop Control

As indicated in the Introduction, this paper mainly focuses on the inner-loop control of HVDC transmission systems, that is, the control at the VSR level. For, in this section it is presented a decentralized, globally asymptotically stabilizing, PI-PBC for the HVDC transmission system (2.9). The construction of the controller is inspired by previous works of the authors on PI-PBC, reported in [16] and [19], which exploit the property of passivity of the *incremental model*. The interested reader is referred to these references for additional details.

As indicated above, it is assumed that a desired operating point $x^* \in \mathcal{E}$ has already been selected—further discussions on its choice are deferred to Sections 6 and 7. To place the proposed PI-PBC in context, in the last part of this section the most commonly used inner-loop controls for HVDC transmission systems are briefly reviewed and a connection is established with the widely popular Akagi's PQ method.

4.1. Passivity of the incremental model

Along the lines of Proposition 1 in [16], it is possible to establish passivity of the incremental model of the overall HVDC transmission system (2.9) with respect to a suitable defined output. As is well-known, global regulation of a passive output can be achieved with a simple PI controller. Regulation of the state to the desired equilibrium then follows provided a suitable detectability assumption is satisfied [35].

Proposition 4.1. Consider the HVDC transmission system (2.9). Let $x^* \in \mathcal{E}$ be the desired equilibrium with corresponding (univocally defined) control $u^* \in \mathbb{R}^{2n}$. Define the error signals

$$\tilde{x} = x - x^*, \quad \tilde{u} = u - u^* \quad (4.1)$$

and the output signal

$$y := \begin{bmatrix} \text{col}(y_{d,i}) \\ \text{col}(y_{q,i}) \end{bmatrix} \in \mathbb{R}^{2n}, \quad (4.2)$$

with

$$y_{d,i} := x_R^{*\top} Q_R \mathcal{J}_{Rd,i} Q_{RXR}, \quad y_{q,i} := x_R^{*\top} Q_R \mathcal{J}_{Rq,i} Q_{RXR}.$$

The mapping $\tilde{u} \rightarrow y$ is *passive*. More precisely, the system verifies the dissipation inequality

$$\dot{\mathcal{H}}_d \leq y^\top \tilde{u}, \quad (4.3)$$

with storage function $\mathcal{H}_d(\tilde{x}) = \frac{1}{2} \tilde{x}^\top Q \tilde{x}$.

Proof. The proof mimics the proof of Proposition 1 in [16]. First of all, it is possible to write

$$\mathcal{J}(u)Qx = \mathcal{J}_0 Qx + g(x)u,$$

where the following definitions

$$\mathcal{J}_0 := \begin{bmatrix} \sum_{i=1}^n (\mathcal{J}_{R0,i} L_{r,i} \omega_i) & -E_3 M \\ M^\top E_3^\top & \mathbf{0}_{\ell \times \ell} \end{bmatrix}, \quad g(x) := \begin{bmatrix} g_{Rd}(x_R) & g_{Rq}(x_R) \\ \mathbf{0}_{\ell \times n} & \mathbf{0}_{\ell \times n} \end{bmatrix},$$

with

$$\begin{aligned} g_{Rd}(x_R) &:= [\mathcal{J}_{Rd,1} Q_{RXR} \quad \dots \quad \mathcal{J}_{Rd,n} Q_{RXR}], \\ g_{Rq}(x_R) &:= [\mathcal{J}_{Rq,1} Q_{RXR} \quad \dots \quad \mathcal{J}_{Rq,n} Q_{RXR}] \end{aligned}$$

are adopted. Hence, it is possible to write (2.9) in the alternative form

$$\begin{aligned} \dot{x} &= (\mathcal{J}_0 - \mathcal{R})Qx + EV + g(x)u \\ &= (\mathcal{J}_0 - \mathcal{R})Q(\tilde{x} + x^*) + EV + g(x)(\tilde{u} + u^*) \\ &= (\mathcal{J}_0 - \mathcal{R})Q\tilde{x} + g(x)\tilde{u} + g(\tilde{x})u^* \end{aligned} \quad (4.4)$$

where (4.1) has been used to get the second equation and the fact that the assignable equilibria x^* and corresponding (constant) control u^* satisfy

$$(\mathcal{J}_0 - \mathcal{R})Qx^* + EV + g(x^*)u^* = 0,$$

is used to obtain the third equation.

The derivative of \mathcal{H}_d along the trajectories of the incremental model (4.4) yields

$$\dot{\mathcal{H}}_d = -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} + \tilde{x}^\top Q g(x) \tilde{u} = -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} + y^\top \tilde{u}$$

where the skew-symmetry of \mathcal{J}_0 , $\mathcal{J}_{Rd,i}$ and $\mathcal{J}_{Rq,i}$ is used in the first equation, and the fact that the output signal can be rewritten as

$$y = g^\top(x^*)Qx = g^\top(x^*)Q\tilde{x}$$

is used to obtain the second identity. The proof is completed recalling that the dissipation matrix verifies $\mathcal{R} > 0$ to obtain the bound (4.3). $\square\square\square$

4.2. PI passivity-based control

The first main result of the paper is then presented.

Proposition 4.2. Consider the HVDC transmission system (2.9), with a desired steady-state $x^* \in \mathcal{E}$, in closed-loop with the decentralized PI control

$$u = -K_P y - K_I \zeta, \quad \dot{\zeta} = y, \quad (4.5)$$

with y given in (4.2) and gain matrices

$$K_P = \begin{bmatrix} K_{Pd} & 0 \\ 0 & K_{Pq} \end{bmatrix} \in \mathbb{R}^{2n \times 2n}, \quad K_I = \begin{bmatrix} K_{Id} & 0 \\ 0 & K_{Iq} \end{bmatrix} \in \mathbb{R}^{2n \times 2n}, \quad (4.6)$$

where $K_{Pd} = \text{diag}\{k_{Pd,i}\}$, $K_{Pq} = \text{diag}\{k_{Pq,i}\}$, $K_{Id} = \text{diag}\{k_{Id,i}\}$, $K_{Iq} = \text{diag}\{k_{Iq,i}\}$. The equilibrium point $(x^*, K_I^{-1}u^*)$ is globally asymptotically stable (GAS).

Proof. Define the Lyapunov function candidate

$$W(\tilde{x}, \tilde{\zeta}) := \mathcal{H}_d(\tilde{x}) + \frac{1}{2} \tilde{\zeta}^\top K_I \tilde{\zeta}, \quad (4.7)$$

where $\tilde{\zeta} := \zeta - K_I^{-1} u^*$. The derivative of $W(x, \zeta)$ along the trajectories of the closed-loop system (2.9)-(4.5) is given by

$$\begin{aligned} \dot{W} &= -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} + y^\top \tilde{u} + \tilde{\zeta}^\top K_I y \\ &= -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} + y^\top \tilde{u} - (\tilde{u}^\top + y^\top K_P) y \\ &= -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} - y^\top K_P y \leq 0, \end{aligned}$$

which proves global stability. Asymptotic stability follows, as done in [16], using LaSalle's arguments. Indeed, from the inequality above and the definition of \mathcal{R} in (2.11) it is clear that all components of the error state vector \tilde{x} tend asymptotically to zero. $\square\square\square$

Remark 4.3. The proposed PI-PBC is decentralized in the sense that, for its implementation, each VSR control requires only the measurement of its corresponding inductor currents and capacitor voltage. Guaranteeing this property motivates the choice of block diagonal gain matrices (4.6).

Remark 4.4. The PI-PBC requires the selection of the desired values for the inductor currents and capacitor voltages that, clearly, cannot all be chosen arbitrarily. Instead, they have to be selected from the set of assignable equilibrium points \mathcal{E} , that is determined by the PFSSE. This set has a rather simple structure: the quadratic equation (3.2) defines the VSRs variables from which it is possible to *univocally* determine the transmission lines coordinates via (3.1).

4.3. Other inner-loop controllers reported in the literature

In this section some of the inner-loop controllers for VSRs reported in the literature are reviewed. The vast majority of the papers reported on this topic—and, in general, of control of power converters [21, 26]—uses the description of the dynamics in co-energy variables. To facilitate the reference to these works, the following model—that is immediately obtained from (2.2) and (2.12)—is provided⁴:

$$\begin{aligned} L \dot{i}_d &= -R i_d + L \omega i_q - v_C u_d + v_d \\ L \dot{i}_q &= -L \omega i_d - R i_q - v_C u_q \\ C \dot{v}_C &= i_d u_d + i_q u_q - G v_C - i_{dc}. \end{aligned} \quad (4.8)$$

The total energy of the VSR is

$$\mathcal{H}(i_d, i_q, v_C) := \frac{1}{2} (L i_d^2 + L i_q^2 + C v_C^2),$$

and the power balance is

$$\dot{\mathcal{H}} = -R(i_d^2 + i_q^2) - G v_C^2 + P - P_{dc}, \quad (4.9)$$

⁴For ease of presentation the discussion here is restricted to a *single* VSR. The extension to multiple VSRs being straightforward.

where the active and DC powers are defined as

$$P = v_d i_d, \quad P_{dc} = v_C i_{dc}. \quad (4.10)$$

It is also common to define the reactive power as $Q = v_d i_q$.

A caveat regarding the subsequent analysis is, however, necessary. When the VSRs are connected to the transmission lines the currents i_{dc} are linked to the currents on the line via (2.7), which are clearly nonconstant. However, to simplify the analysis, it is assumed that they are *constant*. This can be justified by exploiting the fact that their rate of change is slow (with respect to the VSR dynamics). Under this assumption the assignable equilibrium set of (4.8) is given as

$$\mathcal{E} = \{x \in \mathbb{R}^3 \mid R(i_d^2 + i_q^2) - v_d i_d + G v_C^2 + i_{dc} v_C = 0\}. \quad (4.11)$$

Since v_d and i_{dc} are constant, it is then clear that the regulation of P , Q and P_{dc} are equivalent to the regulation of i_d , i_q and v_C , respectively. In practice, because of the small losses of the VSR, the value of P slightly differs from P_{dc} , and consequently there is no interest in regulating the pair i_d and v_C at the same time.

In the literature, it is common to distinguish two modes of operation for a VSR:

- *PQ control mode*, when the VSR is required to control the active and reactive power. This is achieved regulating to zero the output

$$y_I = \begin{bmatrix} i_d - i_d^{\text{ref}} \\ i_q - i_q^{\text{ref}} \end{bmatrix}, \quad (4.12)$$

where the superscript $(\cdot)^{\text{ref}}$ is used to denote reference values—that *do not* necessarily belong to the assignable equilibrium set. These kind of schemes are also called *direct current control* [32].

- *DC voltage control mode*, when the VSR is required to control reactive power and DC voltage. In this case, the regulated output is

$$y_V = \begin{bmatrix} v_C - v_C^{\text{ref}} \\ i_q - i_q^{\text{ref}} \end{bmatrix}. \quad (4.13)$$

These kind of schemes are also called *direct output voltage control* [32].

To regulate the outputs (4.12) and (4.13) different controllers have been proposed in the literature, ranging from simple PI control [23, 26] to feedback linearization [8, 9, 33]. Some of these papers include (invariably local) stability analysis. In Section 5 it is proved that y_I and y_V , used for the PIs or with respect to which feedback linearization is performed, have *unstable zero dynamics*. Consequently, applying high gains in the PIs will induce instability and the internal behavior of the feedback linearizing schemes will be unstable.⁵ Simulations in Subsec-

⁵This well-known phenomenon of nonlinear systems [17] is akin to cancellation of unstable zeros of the plant with the unstable poles of the controller in linear systems.

tion 5.4 show that instability indeed arises for these schemes.

For the sake of comparison the passive output (4.2) in co-energy variables, for a single VSR, is provided:

$$y = \begin{bmatrix} v_C^* i_d - i_d^* v_C \\ v_C^* i_q - i_q^* v_C \end{bmatrix}, \quad (4.14)$$

where $(i_d^*, i_q^*, v_C^*) \in \mathcal{E}$, that is, they belong to the assignable equilibrium set.

Remark 4.5. The PI-PBC is *universal*, in the sense that it can operate either in *PQ* or *DC voltage control mode*, depending on which equilibria are assigned as desired references, and which one is consequently determined via the PFSSE. One important advantage of this universal feature is that there is *no need to switch* between different controllers when the VSRs are requested to change their mode of operation—this is in contrast with other inner-loop schemes that require switchings between controllers, which is clearly undesirable in practice.

4.4. Relation of PI-PBC with Akagi's PQ method

A dominant approach for the design of controllers for reactive power compensation using active filters (for three-phase circuits) is the PQ instantaneous power method proposed by Akagi, *et al.* in [2]. It consists of an outer-loop that generates references for the inner PI loops. The references are selected in order to satisfy a very simple heuristic: the AC active power P has to be equal to the DC power P_{dc} , thus ensuring the maximal power transfer from the AC to the DC side, and the reactive power should take a desired value. Now, using (4.10) define the active AC and DC powers at the equilibrium as

$$P^* = v_d i_d^*, \quad P_{dc}^* = v_C^* i_{dc}^*.$$

Consider then the following equivalences

$$P^* P_{dc} = P_{dc}^* P \Leftrightarrow v_C^* i_d = i_d^* v_C \Leftrightarrow y_1 = 0,$$

with y_1 the first component of the passive output (4.14). Similarly, for the reactive power

$$Q^* P_{dc} = P_{dc}^* Q \Leftrightarrow v_C^* i_q = i_q^* v_C \Leftrightarrow y_2 = 0,$$

with y_2 the second component of the passive output (4.14). In other words, the objective of the PI-PBC to drive the passive output y to zero can be reinterpreted as a power equalization objective identical to the one used in Akagi's PQ method.

5. Performance Limitations of Inner-Loop PIs

Quality assessment of control algorithms is a difficult task—epitomized by the classical performance versus robustness tradeoff, neatly captured by the stability margins in linear designs. The situation for nonlinear systems, where the notions of (dominant) poles and frequency response are specious, is far more complicated. In any case, it is well-known that the achievable performance in control systems is limited by the presence of minimum phase zeros [14, 27, 30].

In this section an attempt is made to evaluate the performance limitations of the inner-loop PI controllers discussed in the previous section. Towards this end, the zero dynamics of the VSR system (4.8) for the outputs y (4.2), y_I (4.12) and y_V (4.13) are computed. All three outputs have relative degrees $\{1, 1\}$, hence their zero dynamics is of order one but, while it is exponentially stable for the passive output y it turns out that—for normal operating regimes of the VSR—it is *unstable* for y_I and y_V . If the zero dynamics is *unstable* cranking up the controller gains yields an unstable behavior. This should be contrasted with the passive output y that, as shown in Proposition 4.2 yields an asymptotically stable closed-loop system for all positive gains.⁶

To simplify the derivations only the case of $i_q^* = 0$ is considered. This assumption is justified since it corresponds to fix to zero the desired value of the reactive power, which is a common operating mode of VSRs. Moreover, this is done without loss of generality because it is possible to show—alas, with messier calculations—that the stability of the zero dynamics is the same for the case of $i_q^* \neq 0$. This situation may arise when the VSR is associated to an AC grid and not to a renewable energy source. In this section, it is possible to prove that the (first order) zero dynamics associated to (4.2), is “extremely slow”—with respect to the overall bandwidth of the VSR. Since this zero “attracts” one of the poles of the closed-loop system, it stymies the achievement of fast transient responses. This situation motivates the inclusion of an outer-loop controller that generates the references to the inner-loop PI. This modification is presented in Section 6.

5.1. Zero dynamics analysis of the passive output y

Before presenting the main result of this subsection, an important observation is done: the zero dynamics of the VSR model (4.8) and of its corresponding incremental version are the same. Indeed, the zero dynamics describes the behavior of the dynamical system restricted to the set where the output is zero. Since the incremental model dynamics is the *same* as the original model dynamics—simply adding and subtracting a constant—their zero dynamics coincide.

Proposition 5.1. Fix $(i_d^*, i_q^*, v_C^*) \in \mathcal{E}$ with $i_q^* = 0$. The zero dynamics⁷ of the VSR (4.8) with respect to the output (4.14) is *exponentially stable* and is given by

$$\dot{v}_C = -\lambda v_C + \lambda v_C^*, \quad \lambda := \frac{R(i_d^*)^2 + G(v_C^*)^2}{L(i_d^*)^2 + C(v_C^*)^2}. \quad (5.1)$$

Proof. By setting the output (4.14) identically to zero and using the fact that $i_q^* = 0$, it is easy to get

$$i_d = \frac{i_d^*}{v_C^*} v_C, \quad i_q = \frac{i_q^*}{v_C^*} v_C = 0. \quad (5.2)$$

⁶This discussion pertains only to the behavior of the adopted mathematical model of the VSR. In practice, other dynamical phenomena and unmodeled effects may trigger instability even for the PI-PBC.

⁷With some abuse of notation, the zero dynamics is represented using the same symbols of the system dynamics.

Replacing (5.2) into (4.8) gives

$$L \frac{i_d^*}{v_C^*} \dot{v}_C = -R \frac{i_d^*}{v_C^*} v_C - v_C u_1 + v_d, \quad (5.3)$$

$$0 = -L \omega \frac{i_d^*}{v_C^*} v_C - v_C u_2, \quad (5.4)$$

$$C \dot{v}_C = \frac{i_d^*}{v_C^*} v_C u_1 - G v_C - i_{dc}. \quad (5.5)$$

To eliminate u_1 it suffices to multiply (5.5) by $\frac{v_C^*}{i_d^*}$ and add it to (5.3), yielding

$$\left(\frac{C v_C^*}{i_d^*} + \frac{L i_d^*}{v_C^*} \right) \dot{v}_C = - \left(\frac{R i_d^*}{v_C^*} + \frac{G v_C^*}{i_d^*} \right) v_C + v_d - \frac{v_C^*}{i_d^*} i_{dc}.$$

The proof is completed by noting from (4.11) that, for $(i_d^*, i_q^*, v_C^*) \in \mathcal{E}$ with $i_q^* = 0$, it follows that

$$v_d - \frac{v_C^*}{i_d^*} i_{dc} = \frac{R (i_d^*)^2 + G (v_C^*)^2}{i_d^*}$$

and by pulling out the common factor $\frac{1}{i_d^* v_C^*}$. $\square\square\square$

Remark 5.2. The parameters R and G , that represent the losses in the VSR, are usually small—compared to L and C . Consequently, λ will also be a small value, placing the pole of the zero dynamics very close to the origin and inducing slow convergence.

Remark 5.3. It is interesting to note that the rate of exponential convergence of the zero dynamics can be rewritten as

$$\lambda = \frac{1}{2} \frac{P^* - P_{dc}^*}{\mathcal{H}(i_d^*, i_q^*, v_C^*)},$$

that is half the ratio between the steady-state dissipated power and the steady-state energy of the system. This relationship holds true also for the case $i_q^* \neq 0$.

5.2. Zero dynamics analysis of y_I

Before analyzing the zero dynamics of the PQ and DC voltage control outputs, (4.12) and (4.13), respectively, it is important to recall that their references do not necessarily belong to the assignable equilibrium set. However, the reasonable assumption that the zero dynamics admits an *equilibrium* for the chosen reference values can be done. If this is not the case the zero dynamics is unstable. Moreover, similarly to the case of the passive output, it is assumed that $i_q^{\text{ref}} = 0$.

Proposition 5.4. Fix $i_d^{\text{ref}} \in \mathbb{R}$, $i_q^{\text{ref}} = 0$. The zero dynamics of the VSR (4.8) with respect to the output (4.12) is given by

$$C \dot{v}_C = -G v_C + \frac{\alpha_I}{v_C} - i_{dc}^{\text{ref}}, \quad \alpha_I := v_d i_d^{\text{ref}} - R (i_d^{\text{ref}})^2 \quad (5.6)$$

where i_{dc}^{ref} is a constant value for i_{dc} satisfying

$$(i_{dc}^{\text{ref}})^2 > 4G\alpha_I. \quad (5.7)$$

- If $\alpha_I > 0$ the zero dynamics has one equilibrium and it is *stable*.
- If $\alpha_I < 0$ the zero dynamics has two equilibria one stable and one unstable.
- If $\alpha_I = 0$ the zero dynamics is a linear asymptotically *stable* system.

Proof. Setting the output (4.12) equal to zero with $i_q^* = 0$ and replacing into (4.8) gives

$$0 = -R i_d^{\text{ref}} - v_C u_1 + v_d \quad (5.8)$$

$$0 = -L \omega i_d^{\text{ref}} - v_C u_2 \quad (5.9)$$

$$C \dot{v}_C = i_d^{\text{ref}} u_1 - G v_C - i_{dc}^{\text{ref}}, \quad (5.10)$$

where the superscript $(\cdot)^{\text{ref}}$ has been added to i_{dc} . Replacing u_1 obtained from (5.8) into (5.10) yields directly (5.6). Condition (5.7) is then necessary and sufficient for the existence of a (real) equilibrium of (5.6). If $\alpha_I = 0$ the dynamics reduces to

$$C \dot{v}_C = -G v_C - i_{dc}^{\text{ref}}.$$

The proof is completed by recalling that $v_C > 0$ and looking at the plots of the right hand side of (5.6) for α_I positive and negative in Fig. 3. $\square\square\square$

Remark 5.5. From Fig. 3, if $\alpha_I < 0$, it is easy to see that the stable equilibrium point is the largest one. For standard values of the system parameters it turns out that this equilibrium is located beyond the physical operating regime of the system, hence it is of no practical interest.

Remark 5.6. The parameters R and G are usually very small and i_{dc}^{ref} can take positive or negative values in standard operation. Then condition (5.7) is always verified while α_I can take positive or negative values.

Remark 5.7. The situation $\alpha_I = 0$, when the zero dynamics is linear and asymptotically stable, is unattainable in applications. Indeed, assuming that in steady-state all signals converge to their reference values, it can be shown that $\alpha_I = 0$ if and only if $G v_C + i_{dc} = 0$ that, given the small values of G is not realistic in practice.

5.3. Zero dynamics analysis of y_V

Proposition 5.8. Fix $v_C^{\text{ref}} \in \mathbb{R}$, $i_q^{\text{ref}} = 0$. The zero dynamics of the VSR (4.8) with respect to the output (4.13) is given by

$$L \frac{di_d}{dt} = -R i_d - \frac{\alpha_V}{i_d} + v_d, \quad \alpha_V := i_{dc}^{\text{ref}} v_C^{\text{ref}} + G (v_C^{\text{ref}})^2 \quad (5.11)$$

where i_{dc}^{ref} is a constant value for i_{dc} satisfying

$$v_d^2 > -4R\alpha_V. \quad (5.12)$$

- If $\alpha_V < 0$ the zero dynamics has two equilibria and they are both *stable*.

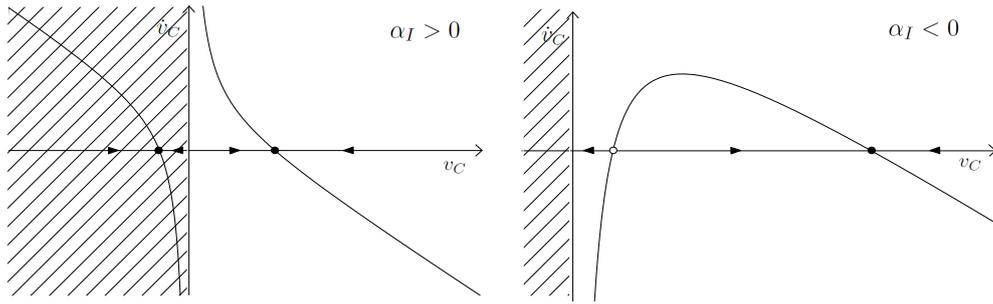


Figure 3: Plot of v_C versus v_C for the cases of (a) $\alpha_I > 0$ and (b) $\alpha_I < 0$. The arrows in the horizontal axis indicate the direction of the flow of the zero dynamics.

- If $\alpha_V > 0$ the zero dynamics has two equilibria one stable and one unstable.
- If $\alpha_V = 0$ the zero dynamics is a linear asymptotically stable system.

Proof. Setting the output (4.13) equal to zero with $i_q^* = 0$ and replacing into (4.8) gives

$$L \frac{di_d}{dt} = -Ri_d - v_C^{\text{ref}} u_1 + v_d, \quad (5.13)$$

$$0 = -L\omega i_d - v_C^{\text{ref}} u_2, \quad (5.14)$$

$$0 = i_d u_1 - Gv_C^{\text{ref}} - i_{dc}. \quad (5.15)$$

Replacing u_1 obtained from (5.15) into (5.13) yields directly (5.11). Condition (5.12) is necessary and sufficient for the existence of a (real) equilibrium of (5.11). The proof is completed invoking the same arguments used in the proof of Proposition 5.4 and are omitted for brevity. $\square\square\square$

Remark 5.9. Remarks 5.6 and 5.7 apply *verbatim* to (5.11) and α_V of Proposition 5.8.

5.4. Simulated evidence of the performance limitations

Although Proposition 5.1 proves that the zero dynamics for the passive output y is exponentially stable, it turns out that, for the components used in standard HVDC transmission system, the convergence rate is $\lambda \approx 0.04$, which is extremely slow. As indicated above this dominating dynamics stymies the achievement of fast transient responses—a situation that is shown in the following simulations. Also, simulated evidence of the unstable behavior of the PI inner-loops using the outputs (4.12) and (4.13) is presented.

A three-terminals HVDC transmission system with a simple *meshed* topology is considered, as illustrated in Fig. 4, where the corresponding graph is also given. The model of the system is given by (2.9), that is a system of dimension $3n + \ell = 11$ with $2n = 6$ inputs. Parameters of the VSRs and of the transmission lines are given in Table 1.

Consider then the following *control objectives*: all the stations are required to regulate the reactive power to zero; the stations associated to the wind farms (WF1, WF2) are required

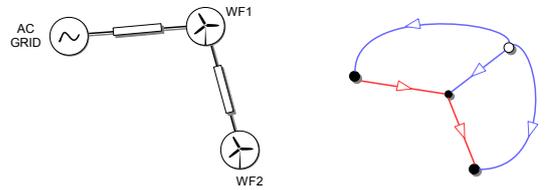


Figure 4: Schematic representation of an HVDC transmission system constituted by three stations, associated to two wind farms (WFs) and an AC grid, with associated graph. The graph is represented by filled circles for the VSRs-buses and the unfilled circle for the ground node. Blue and red edges characterize VSRs and lines, respectively.

Table 1: System parameters.

	Value		Value
$R_{r,i}$	0.01Ω	$G_{r,i}$	$0 \Omega^{-1}$
$L_{r,i}$	40 mH	$C_{r,i}$	$20 \mu\text{F}$
V_i	130 kV	ω_i	50 Hz
$R_{\ell,12}$	26Ω	$L_{\ell,12}$	3.76 mH
$R_{\ell,23}$	20Ω	$L_{\ell,23}$	2.54 mH

to regulate the active power to desired (constant) values; the remaining station, called *slack bus* (SB), must regulate the voltage around its nominal value. In Table 2, the corresponding references of direct current and DC voltages are furnished, together with the corresponding assignable equilibria, that are calculated via the PFSSE defined by (3.3). Changes in references occur every T s over a time interval of $5T$ s. It should be noticed that from 0 to $2T$ the power flow is uniquely directed from both wind farms stations to the AC grid, while at $2T$ and next $3T$ the wind farms stations start demanding power to the AC grid, thus reversing the direction of the power flow. This situation can arise when the power produced by the wind farms is insufficient to supply local loads.

5.4.1. PI-PBC

In this subsection the simulations on the three-terminals benchmark example of the decentralized PI-PBC defined in Subsection 4.2 are presented, illustrating the stability properties

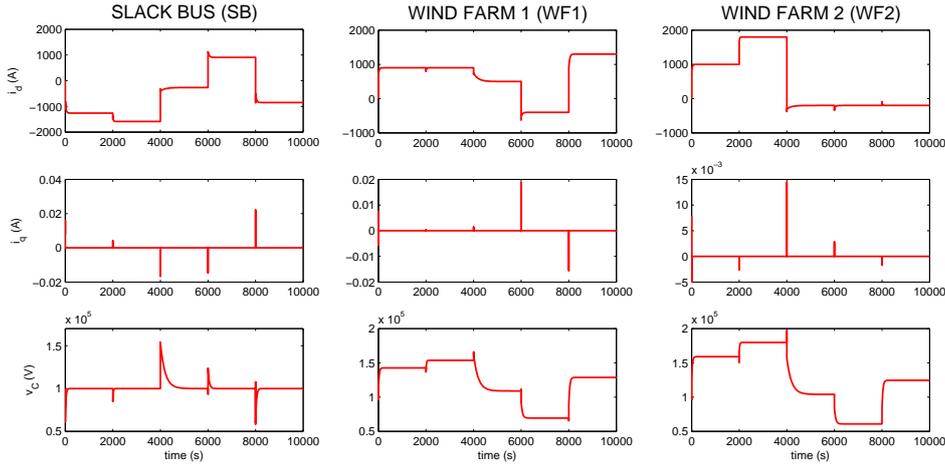


Figure 5: Responses of VSRs variables under the decentralized PI-PBC.

Table 2: System references.

	SB	WF_1	WF_2	SB	WF_1	WF_2
0	-1260	900	1000	100	142.595	158.951
T	-1588	900	1800	100	153.650	179.691
$2T$	-266	500	-200	100	109.004	104.004
$3T$	905	-400	-200	100	69.419	60.877
$4T$	-849	1300	-200	100	128.708	124.532

and performance limitations previously discussed. Setting $T = 2000$ s the controllers (4.5) are designed with identical parameters and diagonal matrices $k_{P,i} = \text{diag}\{1, 1\}$, $k_{I,i} = \text{diag}\{10, 10\}$. The behavior of the VSRs are depicted in Fig. 5.

As expected, the direct currents of each station attain the assignable equilibria defined in Table 2, while the quadrature currents are always kept to zero after a very short transient. Moreover, the DC voltage at the slack bus is maintained near the nominal value of 100 kV, as required, while the DC voltage variation at the wind farms stations, balances the fluctuation of power demand. Even though the desired steady-state is attained for all practical purposes, the convergence time of direct currents and DC voltages is extremely slow. This poor transient performance behavior is independent of the controller gains. Indeed, extensive simulations show that the system maintains the same slow convergence time even with larger gains, thus validating the performance limitations analysis realized in subsection 5.1.

5.4.2. PQ and DC voltage controllers

The behavior of the system under the standard PQ and DC voltage controllers of Subsection 4.3 is next analyzed. In agreement with the control requirements described above, two PQ controllers are designed to regulate direct and quadrature currents of the wind farm stations and one DC voltage controller is designed to regulate DC voltage and quadrature current of the slack bus. Simple PI controllers defined over the outputs (4.12), (4.13) are considered, designed with identical gains $k_{P,i}$,

$k_{I,i}$ tuned via simulations. The behavior of the VSRs are depicted in Fig. 6, with $T = 4$ s. This value should be contrasted with the value ($T = 2000$ s) used for the PI-PBC. It is easy to see that the PQ and DC voltage controllers correctly (and rapidly) regulate the station at the desired references between 0 and 8 s. This good behavior is not surprising, because PQ controllers applied to VSRs that are injecting power and a DC voltage controller applied to VSRs that is absorbing power, have associated globally asymptotically stable zero dynamics, as proved in Subsections 5.2, 5.3. On the other hand, as shown in the figures, when at stations $WF1$ and $WF2$ the power flow is reversed (respectively at $t = 12$ s and $t = 8$ s), the correspondent DC voltages go unstable, because in these cases the zero dynamics is unstable. Similar unstable behavior appears also at the slack bus station.

6. Adding an outer-loop to the PI-PBC

To overcome the transient performance limitations of the PI-PBC exhibited in Subsection 5.4.1, in this section it is proposed to add an outer-loop that takes as input some desired references—indicated with $(\cdot)^{\text{ref}}$ —and generates as output the references to the inner-loop scheme—see Fig. 7. The latter will replace the desired equilibria in the definition of the passive output (4.14), associated to each VSR. Because in this paper the discussion is restricted to the performances of the PI-PBC in nominal operating conditions, the following assumption is made.

Assumption 6.1. *The input references $(\cdot)^{\text{ref}}$ of the outer-loop control belong to the set of assignable equilibria \mathcal{E} .*

In common practice, the references taken as input by the outer-loop control may not belong to the assignable set, thus posing the more general problem of designing an outer-loop that ensures convergence to an (a priori unknown) equilibrium point, *i.e.* the so-called *primary control* problem [10, 39, 3]. However, in the present case the attention is exclusively focused

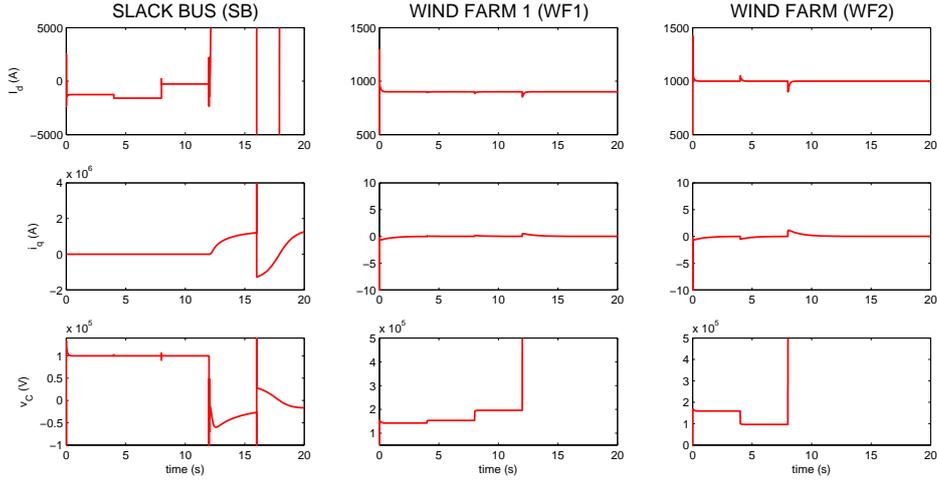


Figure 6: Responses of VSRs variables under the decentralized PQ and DC voltage controllers.

on the aspect of overcoming of performance limitations and a robustness analysis is left for future investigation.

6.1. New output with droop control

A commonly used outer-loop control is the so-called *conventional droop control*, which replaces—at the i -th VSR—the direct current $i_{d,i}^*$ with its desired reference $i_{d,i}^{\text{ref}}$ plus a deviation (droop) term proportional to the voltage error, leaving some constant references for $i_{q,i}^*$ and $v_{C,i}^*$. More precisely, the following assignments are made in (4.14)

$$i_{d,i}^* \leftarrow i_{d,i}^{\text{ref}} + k_{d,i}(v_{C,i}^{\text{ref}} - v_{C,i}), \quad i_{q,i}^* \leftarrow i_{q,i}^{\text{ref}}, \quad v_{C,i}^* \leftarrow v_{C,i}^{\text{ref}}, \quad (6.1)$$

where $k_{d,i} > 0$ is called the droop coefficient. Replacing (6.1) in the passive output (4.14) yields the new output

$$y_{N,i} := \begin{bmatrix} v_{C,i}^{\text{ref}} i_{d,i} - i_{d,i}^{\text{ref}} v_{C,i} - k_{d,i}(v_{C,i}^{\text{ref}} - v_{C,i}) v_{C,i} \\ v_{C,i}^{\text{ref}} i_{q,i} - i_{q,i}^{\text{ref}} v_{C,i} \end{bmatrix}. \quad (6.2)$$

The simulations show that the performance of the modified PI-PBC, that is, adding a PI around the new outputs (6.2), is significantly better than the original PI-PBC and that, under Assumption 6.1, the same closed-loop equilibrium point is preserved.

Remark 6.2. In several papers, *e.g.*, [3, 33], the assumption that the inner-loop PI and the VSRs are much faster than the network dynamics is made. Under this assumption, a globally asymptotically stabilized VSR can be equivalently modeled as the parallel interconnection of two AC current sources connected to the network through a voltage bus capacitor, that is assumed to operate at the same time scale of the network. The resulting reduced model is linear and conditions for *local* asymptotic stability can be easily established [3, 39].

Remark 6.3. In contrast to [3, 31], the droop control laws are defined with respect to DC voltages that do not necessarily all coincide with the same nominal value v_C^{nom} . Then, the

trade-off between having the voltages converge to the nominal voltage, and satisfying a pre-determined active power distribution (*sharing*) between the VSRs, is completely captured by the power flow steady-state equations that determine the set of assignable equilibria. The reader is referred to Section 7 for further details on this problem.

6.2. A GAS outer-loop controller

It is evident from (6.2), that the inclusion of additional state-dependent terms to the first component of $y_{N,i}$ invalidates the stability result obtained in Proposition 4.2, as the new output $y_{N,i}$ is *not passive*. Moving from these considerations, a modified version of the PI-PBC (4.5) is proposed. The latter, if properly designed, allows to overcome the performance limitations of the passive output, while preserving global asymptotic stability of the closed-loop system. This modification consists of an additional linear feedback that affects only the proportional part of the PI-PBC. The following proposition is then presented.

Proposition 6.4. Consider the HVDC transmission system (2.9), with a desired steady-state $x^* \in \mathcal{E}$, in closed-loop with the PI control

$$u = -K_P y - K_I \zeta - K_L Q \bar{x}, \quad \dot{\zeta} = y, \quad (6.3)$$

with y given in (4.2), gain matrices K_P, K_I as in (4.6) and $K_L \in \mathbb{R}^{2n \times (3n+l)}$ verifying

$$\mathcal{R}_0 := \mathcal{R} + g(x^*) K_P g^\top(x^*) + \frac{1}{2} \left[g(x^*) K_L + K_L^\top g^\top(x^*) \right] > 0. \quad (6.4)$$

Then, the equilibrium point $(x^*, K_I^{-1} u^*)$ is globally asymptotically stable (GAS).

Proof. Using the same Lyapunov function (4.7) employed in the proof of Proposition 4.2, the derivative along the trajectories

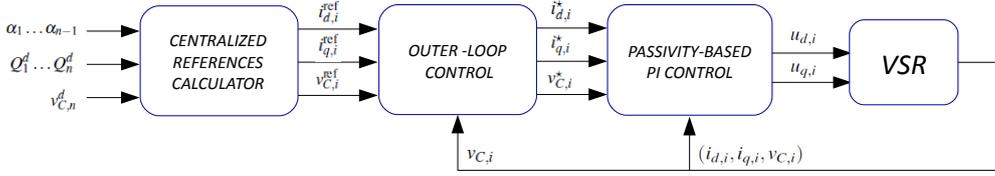


Figure 7: Control architecture of HVDC transmission systems under nominal operating conditions.

of the closed-loop system (2.9)–(6.3) is given by

$$\begin{aligned}
\dot{W} &= -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} + y^\top \tilde{u} + \tilde{\zeta}^\top K_L y \\
&= -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} + y^\top \tilde{u} - (\tilde{u}^\top + y^\top K_P + \tilde{x}^\top Q K_L^\top) y \\
&= -\tilde{x}^\top Q \mathcal{R} Q \tilde{x} - \tilde{x}^\top Q g(x^*) K_P g^\top(x^*) Q \tilde{x} - \tilde{x}^\top Q K_L^\top g^\top(x^*) Q \tilde{x} \\
&= -\tilde{x}^\top Q \mathcal{R}_0 Q \tilde{x} < 0,
\end{aligned}$$

where in the third equivalence the output definition $y = g^\top(x^*) Q \tilde{x}$ is used, while the last equivalence follows from condition (6.4). $\square\square\square$

Loosely speaking, the Proposition 6.3 states that the property of global asymptotic stability of the closed-loop system (2.9)–(4.5)—that is the HVDC transmission system controlled via PI–PBC—is preserved for any additional linear feedback that affects only the proportional part of the controller and any gain matrix K_L which verifies condition (6.4). However, beside this stability result, Proposition 6.4 does not provide any hint on how to select the controller gains in order to overcome the performance limitations of the PI–PBC, nor how to preserve the decentralization property that – for some inappropriate choice of the gain matrix – can be even lost. Taking inspiration from the conventional droop controller discussed in the previous section, the following assignment is made:

$$K_L := \begin{bmatrix} 0 & 0 & K_D & 0 \\ 0 & 0 & 0 & 0 \end{bmatrix},$$

where $K_D := \text{diag}\{k_{D,i}\} \in \mathbb{R}^{n \times n}$ is a positive matrix to be defined. With this choice it is easy to see that the controller (6.3) can be decomposed in n decentralized controllers of the form

$$\begin{bmatrix} u_{d,i} \\ u_{q,i} \end{bmatrix} = \begin{bmatrix} k_{Pd,i} y_{d,i} - k_{Id,i} z_{d,i} - k_{D,i} (v_{C,i} - v_{C,i}^*) \\ k_{Pq,i} y_{q,i} - k_{Iq,i} z_{q,i} \end{bmatrix}, \quad \begin{bmatrix} z_{d,i} \\ z_{q,i} \end{bmatrix} = \begin{bmatrix} y_{d,i} \\ y_{q,i} \end{bmatrix}, \quad (6.5)$$

that correspond to n PI–PBC plus an additional linear feedback in the local DC voltage error. Straightforward calculations—here omitted for brevity—show that it is always possible to determine a gain matrix K_D , such that (6.4) is verified, thus guaranteeing global asymptotic stability of the closed-loop system.

Remark 6.5. The modified PI–PBC (6.5) can be interpreted, similarly to the droop controller (6.1), as an outer-loop provid-

ing references for the the standard PI–PBC, *but only* affecting its proportional part. It is indeed easy to see that it corresponds to assume the following inner-loop control scheme

$$\begin{bmatrix} u_{d,i} \\ u_{q,i} \end{bmatrix} = \begin{bmatrix} k_{Pd,i} (v_{C,i}^{*P} i_{d,i} - i_{d,i}^{*P} v_{C,i}) - k_{Id,i} z_{d,i} \\ k_{Pq,i} (v_{C,i}^{*P} i_{q,i} - i_{q,i}^{*P} v_{C,i}) - k_{Iq,i} z_{q,i} \end{bmatrix}, \quad \begin{bmatrix} z_{d,i} \\ z_{q,i} \end{bmatrix} = \begin{bmatrix} v_{C,i}^{*I} i_{d,i} - i_{d,i}^{*I} v_{C,i} \\ v_{C,i}^{*I} i_{q,i} - i_{q,i}^{*I} v_{C,i} \end{bmatrix},$$

together with the following outer-loop assignments of the proportional and integral references

$$\begin{aligned}
i_{d,i}^{*,P} &\leftarrow i_{d,i}^{\text{ref}} + k_{D,i} \frac{v_{C,i} - v_{C,i}^{\text{ref}}}{v_{C,i}}, & i_{q,i}^{*,P} &\leftarrow i_{q,i}^{\text{ref}}, & v_{C,i}^{*,P} &\leftarrow v_{C,i}^{\text{ref}} \\
i_{d,i}^{*,I} &\leftarrow i_{d,i}^{\text{ref}}, & i_{q,i}^{*,I} &\leftarrow i_{q,i}^{\text{ref}}, & v_{C,i}^{*,I} &\leftarrow v_{C,i}^{\text{ref}},
\end{aligned} \quad (6.6)$$

where, as done before, the notation $(\cdot)^{\text{ref}}$ indicates the (assignable) references of the outer-loop.

6.3. Simulations

To illustrate the previous discussion on outer-loop controllers, the three-terminals benchmark example described in Subsection 5.4—controlled via decentralized PI–PBC—is considered. The same control parameters of Subsection 5.4.1 are employed, and the benefits in terms of performance, provided by adding an outer-loop control to the PI–PBC controllers of the form (6.6), are further analyzed. For the choice of the controller gains a very simple heuristic, often invoked in conventional droop control, is selected. Because droop coefficients are supposed to quantify the additional dissipation injected into the voltage dynamics, and because the rate of convergence of the same depend from the value of the capacitances, define

$$d_i = \frac{G_i + k_{D,i}}{C_i}, \quad i \in [1, n] \quad (6.7)$$

as a measure of the convergence rate of the i -th station, with G_i and $k_{D,i}$ the conductance and the droop coefficient of the VSR, respectively. A possible choice of droop coefficients is to define a common convergence rate d such that $d_i = d$ for every i , that is equivalent to define an uniform convergence rate over the three stations. In the three-terminals benchmark example, because parameters are supposed to be identical at each VSR, the droop coefficients will take identical values, namely $k_{D,i} = 5 \cdot 10^{-2}$. The behavior of the VSRs are illustrated in Fig. 8. In contrast to the simulations of the basic PI–PBC of Subsection

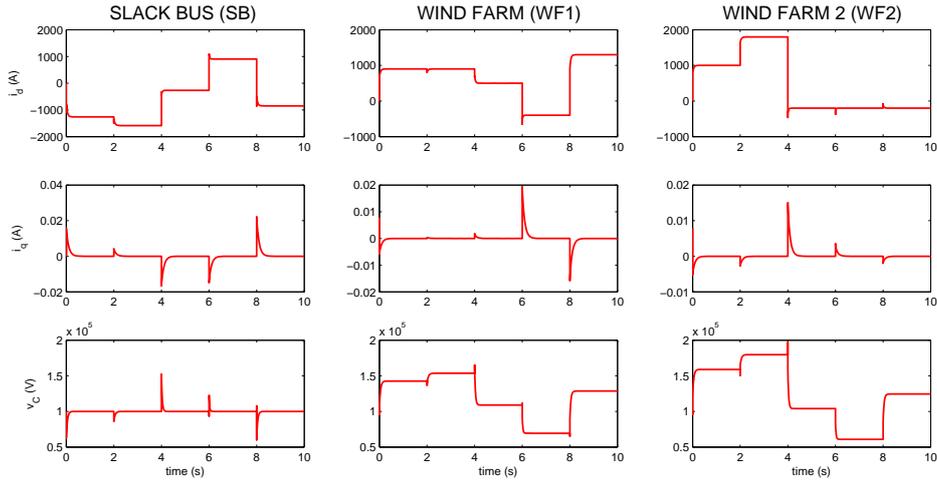


Figure 8: Responses of VSRs variables with the decentralized PI-PBC plus GAS outer controller.

5.4.1 when the references change every $T = 2000$ s, now they are a *thousand times faster* that is, every $T = 2$ s. It is easy to see that, compared to Fig. 5, the responses maintain the same shape while the convergence occurs with a rate $\approx 10^3$ faster.

Remark 6.6. Under Assumption 6.1, the responses of the VSRs under the PI-PBC plus conventional droop control—here omitted for brevity—are very similar to the responses of the GAS outer controller, depicted in Fig. 8. However, if Assumption 6.1 is not verified, *e.g.* in perturbed operating conditions, significant differences occur between the VSRs responses. It is indeed possible to verify that, while the conventional droop control ensures convergence to an (assignable) equilibrium point independently from the assigned references, the GAS outer controller may experience instability.

7. Centralized References Calculator

In this section, a reformulation of the problem of choosing appropriate references for the droop or inner PI controllers presented in the previous sections is provided. In power systems literature, this is often referred as *references calculator* [10] and it is in general characterized by a centralized architecture—see Fig. 7.

It is next shown how certain simple constraints—widely employed for the references calculation—can be mathematically formalized using the PFSSE. It is worth mentioning that the constraints adopted here, are only special cases of a more complex optimization problem, that in general requires to take into account many other aspects, related to technical and economical issues. However, the following analysis is limited to some of the most relevant technical aspects, leaving a more diverse investigation as a future work.

Consider an HVDC transmission system with *meshed* topology composed by n VSRs (stations), and described by the pH system (2.9). The set of assignable references is determined by

the following PFSSE

$$\begin{aligned}
 & -R_i(i_{d,i}^{\text{ref}})^2 - R_i(i_{q,i}^{\text{ref}})^2 - G_i(v_{C,i}^{\text{ref}})^2 + \\
 & + v_{d,i}i_{d,i}^{\text{ref}} - v_{C,i}^{\text{ref}}M_i\mathcal{R}_L^{-1}M_i^\top v_C^{\text{ref}} = 0,
 \end{aligned} \tag{7.1}$$

for $i \in [1, n]$, where the row vector $M_i \in \mathbb{R}^n$ is the i -th row of the matrix M , that coincide with (3.2), but in co-energy variables. The PFSSE consist in n quadratic, coupled equations in $3n$ variables, one for each station. A possibility is then to directly assign $2n$ variables, that correspond to the desired references, while the remaining n variables can be easily determined via (7.1) and then be provided to the inner-loop controllers. However, the choice of which variables have to be chosen as desired references is not arbitrary, but depends on the control objectives to satisfy. It is next illustrated how the problem of defining references in conformity with some natural control objectives can be formalized using the PFSSE.

Assume the following requirements for the n stations: keep the DC voltage of only one station (called *slack bus*) close to the nominal value, guarantee a proportional active power distribution (*power sharing*) among the stations, regulate the reactive power to a desired value at each station. These requirements can be easily reformulated as constraints over the PFSSE as follows:

- regulation of the DC voltage of the *slack bus*

$$v_{C,n}^{\text{ref}} = v_{C,n}^{\text{d}},$$

where $v_{C,n}^{\text{d}}$ represents the DC voltage nominal value;

- proportional *power sharing*

$$P_i^{\text{ref}} = \alpha_i P_n^{\text{ref}} \Rightarrow i_{d,i}^{\text{ref}} = \left[\frac{v_{d,n}}{v_{d,i}} \alpha_i \right] i_{d,n}^{\text{ref}}, \quad i \in [1, n-1]$$

where α_i is a ratio that determines the proportional active power distribution of the i -th station with respect to the

slack bus;

- regulation of the reactive power

$$Q_i^{\text{ref}} = Q_i^{\text{d}} \Rightarrow i_{q,i}^{\text{ref}} = \frac{Q_i^{\text{d}}}{v_{d,i}}, \quad i \in [1, n]$$

where Q_i^{d} represents the exact reactive power required to be injected (or absorbed) by the i -th station.

It is easy to see that the equations above constitute indeed a set of $2n$ assignments for the PFSSE, that can be consequently solved with respect to the remaining variables.

8. Conclusions and Future Perspectives

The present work covers different aspects of modeling, analysis and control of multi-terminal HVDC transmission systems. The main contribution is a decentralized, globally asymptotically stable, PI control for a very general class of multi-terminal HVDC transmission systems. For this purpose, starting from a graph description of the network, a pH representation has been obtained, thus revealing the intrinsic passivity properties of the system. The result is a direct extension of the previous works on PI control of VSRs, to a sufficiently general interconnected system, with the important property that the control is decentralized, a fundamental requirement for large-scale systems. To provide some connections between the proposed controller and standard techniques, widely used in literature, a comparative analysis of stability and performances is provided, shedding some light on limitations and benefits of different approaches. In particular it is proved—and validated via simulations—that the popular current and voltage control techniques possibly lead to unstable behaviors of the controlled system, while the proposed PI-PBC, although ensuring convergence, has clear performance limitations. The theoretical analysis that substantiates these claims is based on a detailed, nonlinear zero dynamics analysis of a single VSR with respect to the outputs used for all these controllers. To overcome the performance limitations of the PI-PBC, an outer-loop controller is added. This outer-loop takes the form of a voltage droop—that is the *de facto* standard in the power systems community. Taking inspiration from its usual formulation an alternative controller that overcomes the performance limitation of the PI-PBC is then developed, further showing that global asymptotic stability is preserved.

A future research line pertains to the use of more accurate models for the description of the system, that may improve the control quality. For instance, the behavior of long transmission lines is best described by means of the Telegrapher's equations, thus leading to an infinite dimensional pH representation, which can still be handled with existing theory [37]. Because the conventional droop controller destroys the passivity property—that is instrumental for the stability analysis of the PI-PBC—current research is under way to establish some stability properties of the PI-PBC plus conventional droop control. A further possibility is the development of new provably stable outer-loop

primary controllers, that ensure convergence to an (a priori unknown) equilibrium point, while the references do not belong to the set of assignable equilibria. Although the latter approach is of theoretical interest, it is the authors' belief that a rigorous analysis of the droop control—to substantiate its widely acknowledged robustification features—would better contribute to bridge the gap between theory and engineering practice. It would also be of interest to investigate in detail new strategies for the references calculation, moving away from the PFSSE. A viable possibility is to consider the latter as a problem of static optimization, that allows a simple characterization of the control objectives. A final, long term, objective is the experimental validation of the proposed PI-PBC plus droop control scheme.

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